NOTICE


It is notified for the information of the students of 4th Sem B.Tech programme 2015 – 16 that the End Semester Exam in Physics of Semi Conductor Devices scheduled to be conducted in 1st Sitting of 16th April’2016 (Saturday) is hereby shifted to the 1st Sitting of 19th April’2016 (Tuesday) under unavoidable circumstances. The students having backlog subject in above subject in their 3rd semester course may appear the same along with regular students provided they are otherwise eligible.

The change may be in-corporated in the Exam schedule already circulated to all concerned vide this Office notice- CVRCE/Exam/078/16 dated – 19.03.2016 for reference.

Copy to:
1. Chairman’s Office/Director’s Office/Principal’s Office/Dean Academic/All HOD’s/MCA/DSW/T&P Cell/Transport I/C/Security I/C/Chief Warden/Student’s Counter/Notice Boards for information & necessary action.
2. SSEPL with a request to display it in the college website.

Director

[Signature]
01.04.16

Director

[Signature]
01.04.16